UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

Page 1 of 2

PATENT NO.

: 7,246,250 B2

APPLICATION NO.: 09/941091

DATED

: June 17, 2007

INVENTOR(S)

: Jung et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In The Claims:

Column 9, Claim 3, Line 52: Please correct "memory device": To read -- memory devices --

Column 10, Claim 8, Line 39: Please correct "according to the" To read -- according to a --

Column 10, Claim 8, Line 43: Please correct "delay information" To read -- control information --

Column 10, Claim 8, Line 44: Please correct "delay control" To read --predetermined control --

Column 10, Claim 8, Line 46: Please correct "respond to the" To read -- respond to an --

Column 11, Claim 12, Line 15: Please correct "receiving delay control" To read -- receiving the control --

Column 11, Claim 12, Line 17: Please correct "received delay control" To read -- received control --

Column 12, Claim 20, Line 12: Please correct "controlling the delay" To read -- controlling delay --

Column 12, Claim 24, Line 51: Please correct "that is based on position" To read -- that is based on a position --

Column 14, Claim 34, Line 37: Please correct "operating an integrated circuit memory" To read -- operating a memory --

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> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14, Claim 34, Line 41: Please correct "information from that is" To read -- information that is --

Signed and Sealed this

Page 2 of 2

Sixteenth Day of October, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office